

VALUE FOR MONEY LOGIC DESIGN TRAINER

LD-2 PENCILBOX

The LD-2 PENCILBOX Logic Designer Trainer permits students to avoid the repetitive building of frequently used circuits.

All functions are internally connected to three solderless interconnect sockets with two tie points for each signal. Comes complete with plug-in power supply (230Vac or 115Vac)

On Board D.C. Supplies: +12 volts $\pm 5\%$ 200 mA max.; -12 volts $\pm 5\%$ 200 mA max., +5 volts $\pm 5\%$ 250 mA max.

TTL/CMOS Operation: Selectable output voltage of clock, pulser, logic switches. Input voltage thresholds of seven segment displays, logic indicators and Vcc tie point between 5 volts, TTL: and 12 volts, CMOS.

Clock: 1 Hz, 1 KHz, 100 KHz (all $\pm 20\%$.)

Also user variable with external capacitor.

Logic "1" output current 2 mA @ 4.0 V min. (5V/TTL); 4 mA @ 11.0 V min. (12V/CMOS)

Logic "0" output current 2 mA @ 0.1 V max. (5 V/TTL); 5mA @ 0.1 V max. (12V/CMOS)

Pulsers: Two fully debounced pushbuttons with true logic and complementary outputs. Logic "1" output current 2 mA @ 2.5V min. (5 V/TTL); 2 mA @ 11.0 V mm. (12 V/CMOS)

Logic "0" output current 2 mA @ 0.4 V max. (5V/TTL); 5 mA @ 0.4 V max. (12 V/CMOS)

Logic Switches: Eight SPDT switches select output of Vcc or 0V. Output current, all cases: 200 mA maximum.

Logic Indicators: Eight LEDs buffered by two four-bit latches with separate enables. Input impedance, all inputs: 100 k ohms.

Seven-Segment Displays: Two BCD-input seven-segment displays with separate Display Enable (DE), Latch Enable (LE), and Lamp Test (LT) inputs. Input impedance, all inputs: 100 K ohms.

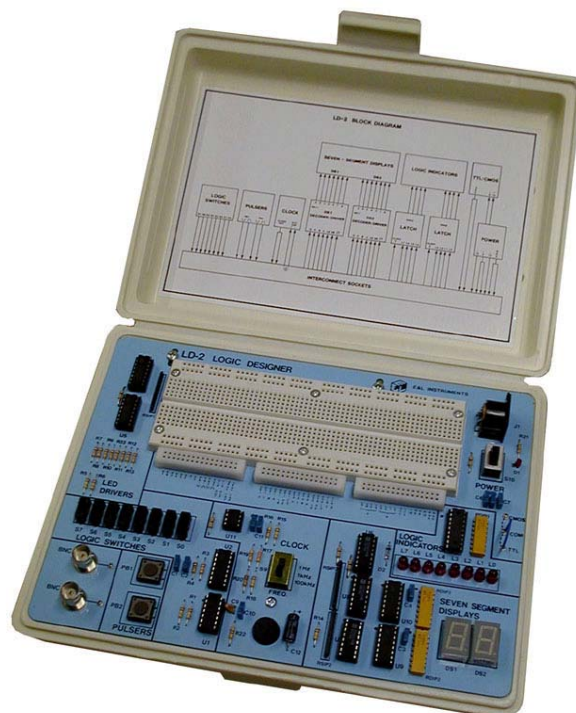
Connectors: All functions are permanently tied to three solderless tie point connectors. Each tie point has two solderless connection points. Gnd (0V), +5 volts, Vcc, and clock out have four connection points each. All tie point functions are marked on the front panel.

BNCs: Two BNC connectors with the shells tied to ground and the pins connected to adjacent single tie points.

Breadboarding: One SK-10 solderless breadboarding socket is permanently attached to the unit. The SK-10 can accommodate up to eight 14-pin DIP ICs with five tie points per pin plus eight power rails with 25 tie points each.

Dimensions: 6.6 x 19.05 x 25.4 cm (H x W x D)(2.6" x 7.5" x 10")

Weight: 1.4 kg (3.5 lb.)



ORDERING INFORMATION	Part Number
LD-2 Pencilbox	325-2302

(Please specify 230V or 115 V AC input when ordering)

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